

AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions and listings of claims in the application.

Listing of Claims:

1-3 (canceled).

4 (currently amended). A [[The]] pilot signal reception method according to claim 3 , comprising:

receiving pilot signals of a pilot channel;

despreading the received pilot signals;

measuring a variation in the reception intensity of a demodulated signal obtained by the despreading; and

adaptively changing despreading timings of the received pilot signals based on the measured variation in the reception intensity, in accordance with random reception timings, wherein the despreading timings are adaptively determined based on the measured variation in the reception intensity in such a way as to avoid valleys of the variation in the intensity of the reception signal.

5 (currently amended). A [[The]] pilot signal reception method according to claim 3, comprising:

receiving pilot signals of a pilot channel;

despreading the received pilot signals;

measuring a variation in the reception intensity of a demodulated signal obtained by said despreading; and

adaptively changing despreading timings of the received pilot signals based on the measured variation in the reception intensity, in accordance with random reception timings, wherein the despreading timings and despreading period are adaptively changed based on the measured variation in the reception intensity in such a way as to avoid valleys of the variation in the intensity of the reception signal.

6-11 (canceled).

12 (previously presented). A receiver that receives pilot signals, comprising:
a despreading circuit that despreads the received pilot signals;
a timing determination circuit that determines the start timing of said despreading based on the intensity and variation of the intensity of a demodulated signal output from said despreading circuit; and

a desreading chip number determination circuit that determines the number of chips to be desread based on the intensity and variation of the intensity of the demodulated signal output from said desreading circuit,

wherein operation of said desreading circuit is controlled based on the determined desreading timing and desreading chip number in such a way as to desread pilot signals adaptively and irregularly.

13-14 (canceled).